

FIG. 1

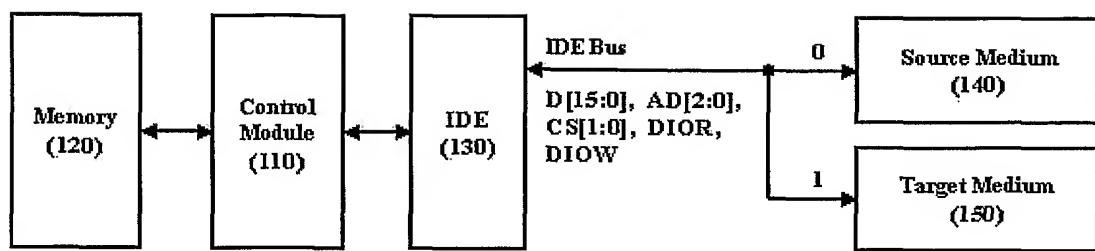


FIG. 2

IDE Function Allocation Address Table (200)

Address					Functions	
CS0-	CSI-	DA2	DA1	DA0	Read (DIO&R-)	Write (DIO&W-)
N	N	x	x	x	Data bus high impedance	Not used
Control block registers						
N	A	0	x	x	Data bus high impedance	Not used
N	A	1	0	x	Data bus high impedance	Not used
N	A	1	1	0	Alternate Status	Device Control
N	A	1	1	1	* * *	Not used
Command block registers						
A	N	0	0	0	Data	Data
A	N	0	0	1	Error	Features
A	N	0	1	0	Sector Count	Sector Count
A	N	0	1	1	Sector Number	Sector Number
A	N	1	0	0	Cylinder Low	Cylinder Low
A	N	1	0	1	Cylinder High	Cylinder High
A	N	1	1	0	Device/Head	Device/Head
A	N	1	1	1	Status	Command
A	A	x	x	x	Invalid address	Invalid address

FIG. 3a

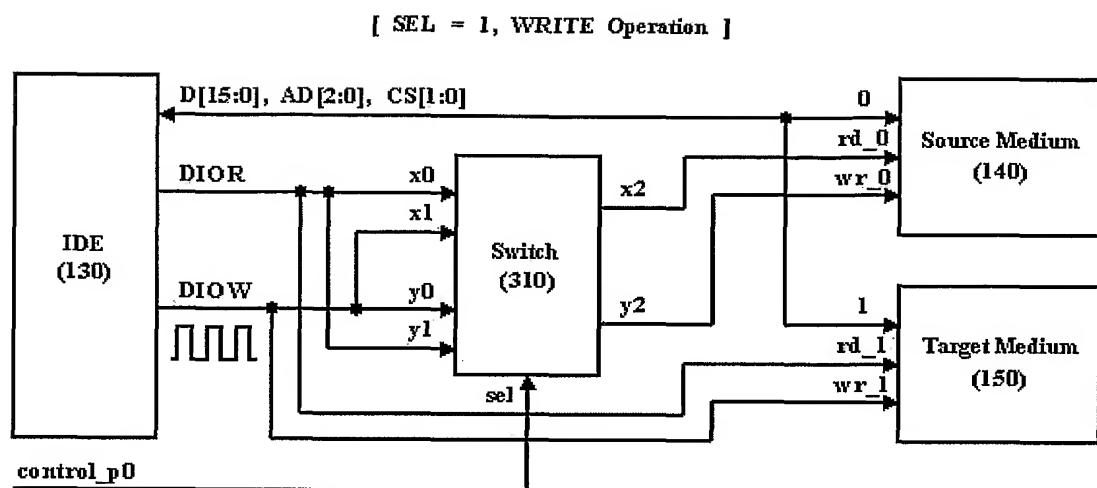


FIG. 3b

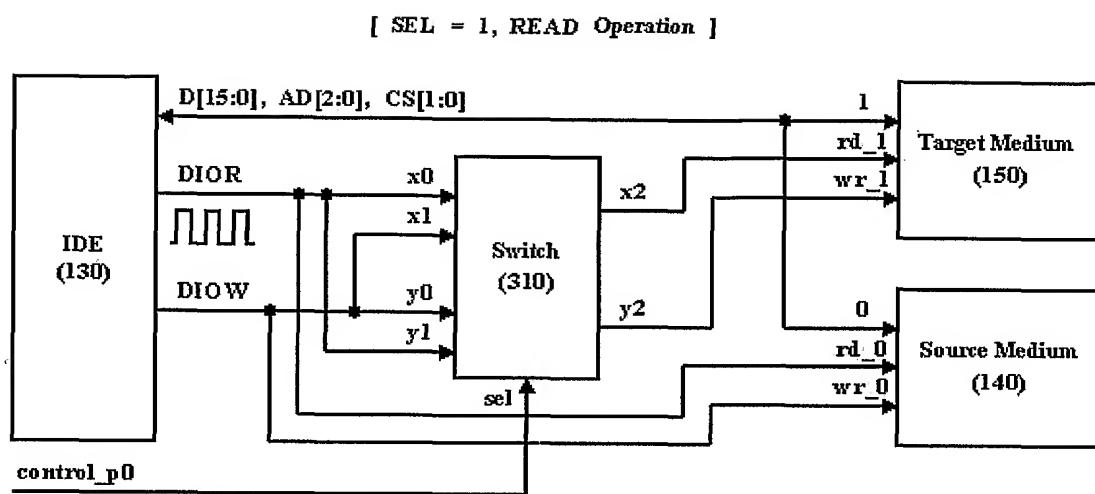


FIG. 4

